

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANTS PTO FORM 1449</b>	<b>ATTY. DOCKET NO.</b> <b>10191/3283</b>	<b>SERIAL NO.</b> <del>Not yet assigned</del> <b>10/613,459</b>
	<b>APPLICANT(s)</b> <b>Wilhelm FREY et al.</b>	
	<b>FILING DATE</b> Herewith	<b>GROUP</b> Not yet assigned

**U. S. PATENT DOCUMENTS**

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
TL	6,210,988	April 3, 2001	Howe et al.			

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
						YES	NO
TL	38 74 411*	October 15, 1992	Fed. Rep. of Germany			Abs.	
TL	0 292 057**	November 23, 1988	Europe			Abs.	

\*Described in Specification

\*\* Corresponds to German Patent No. 38 74 411.

**OTHER DOCUMENTS**

EXAMINER INITIAL		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
TL		Franke et al., "Post-CMOS Modular Integration of Poly-SiGe Microstructures Using Poly-Ge Sacrificial Layers", Solid-State Sensor and Actuator Workshop, Hilton Head, SC, June 2000, pp. 18-21

<b>EXAMINER</b> <i>Thao Li</i>	<b>DATE CONSIDERED</b> <b>12/20/04</b>
<b>EXAMINER:</b> Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	